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Art Unit 2117

Attn: Certificate of Correction Branch

Re: U.S. Utility Patent
Patent No. 7,373,561 B2; Issued: May 13, 2008
For: **Integrated Packet Bit Error Rate Tester for 10G SERDES**
Inventors: Baumer *et al.*
Our Ref: 1875.4430000

Sir:

Transmitted herewith for appropriate action are the following documents:

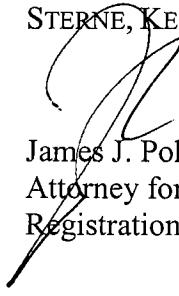
1. Request for Certificate of Correction Under 37 C.F.R. 1.322; and
2. Certificate of Correction (Form PTO/SB/44).

The above-listed documents are filed electronically through EFS-Web.

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.


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JJP/la
Enclosure(s)